

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Adam L Ghozeil et al

Confirmation No.:

Application No.:

Examiner:

Filing Date: Herewith

Group Art Unit:

Title: Array Of Nanoscopic Mosfet Transistors And Fabrication Methods

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)

under 37 CFR 1.97 (c) together with either a:  
 Statement under 37 CFR 1.97(e), or  
 a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)

under 37 CFR 1.97 (d) together with a:  
 Statement under 37 CFR 1.97(e)(1) or (2), and  
 a \$180.00 fee set forth in 37 CFR 1.17(p).  
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Please charge to Deposit Account 08-2025 the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25.

Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individual(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450. Date of Deposit: 7/15/03  
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Typed Name: Timothy F. Myers

Signature: Timothy F. Myers

Respectfully submitted,

Adam L Ghozeil et al

By Timothy F. Myers

Timothy F. Myers

Attorney/Agent for Applicant(s)  
Reg. No. 42,919

Date: 7/15/03

Telephone No.: (541) 715-4197

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	
				First Named Inventor	Adam GHOZEIL et al.
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	200300117

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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				Filing Date	
				First Named Inventor	Adam GHOZEIL et al
				Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	200300117

<b>NON PATENT LITERATURE DOCUMENTS</b>				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
		Y. CHEN et al., Appl. Phys. Letters V. 82(10) (10 Mar. 2003), pp. 1610-1612		
		D-Y. KHANG et al., Appl. Phys. Letters V.75 (17),(25 OCTOBER 1999), pp. 2599-2601		
		X. SUN et al., J. Vac. Sci. Technol. B 16(6), Nov/Dec 1998, pp. 3922-3925		
		L. GUO et al, Appl. Phys. Letters, vol. 71 (Sep. 29, 1997) pp. 1881-1883		
		L. GUO et al., Science V. 275(31), Jan. 1997, pp. 649-651		
		P. R. KRAUSS et al., 1996 54th Annual Device Research Conf, IEEE, June 24-26, 1996 pp. 194-195		
		S. Y. CHOU et al., J. Vac. Sci. Technol. B 14(6), Nov/Dec 1996, pp. 4129-4133		
		S. Y. CHOU et al., Science V. 272 (Apr. 5, 1996), p. 85-87		
		S. Y. CHOU et al., Appl. Phys. Letters V. 76 (1995), pp. 3114-3116		

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